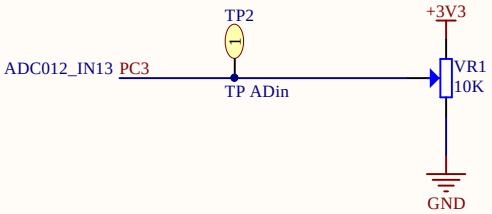
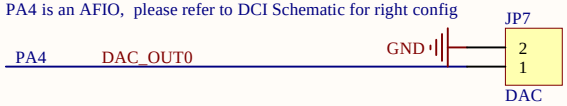
ADC



DAC

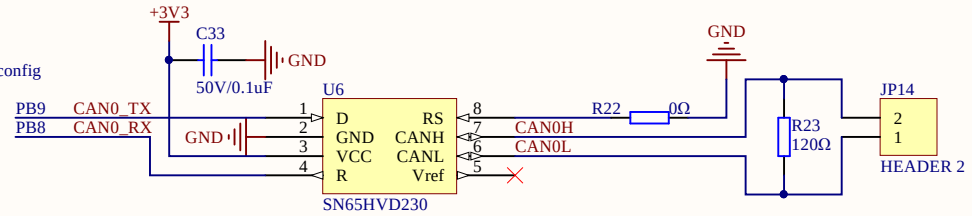
PA4 is an AFIO, please refer to DCI Schematic for right config



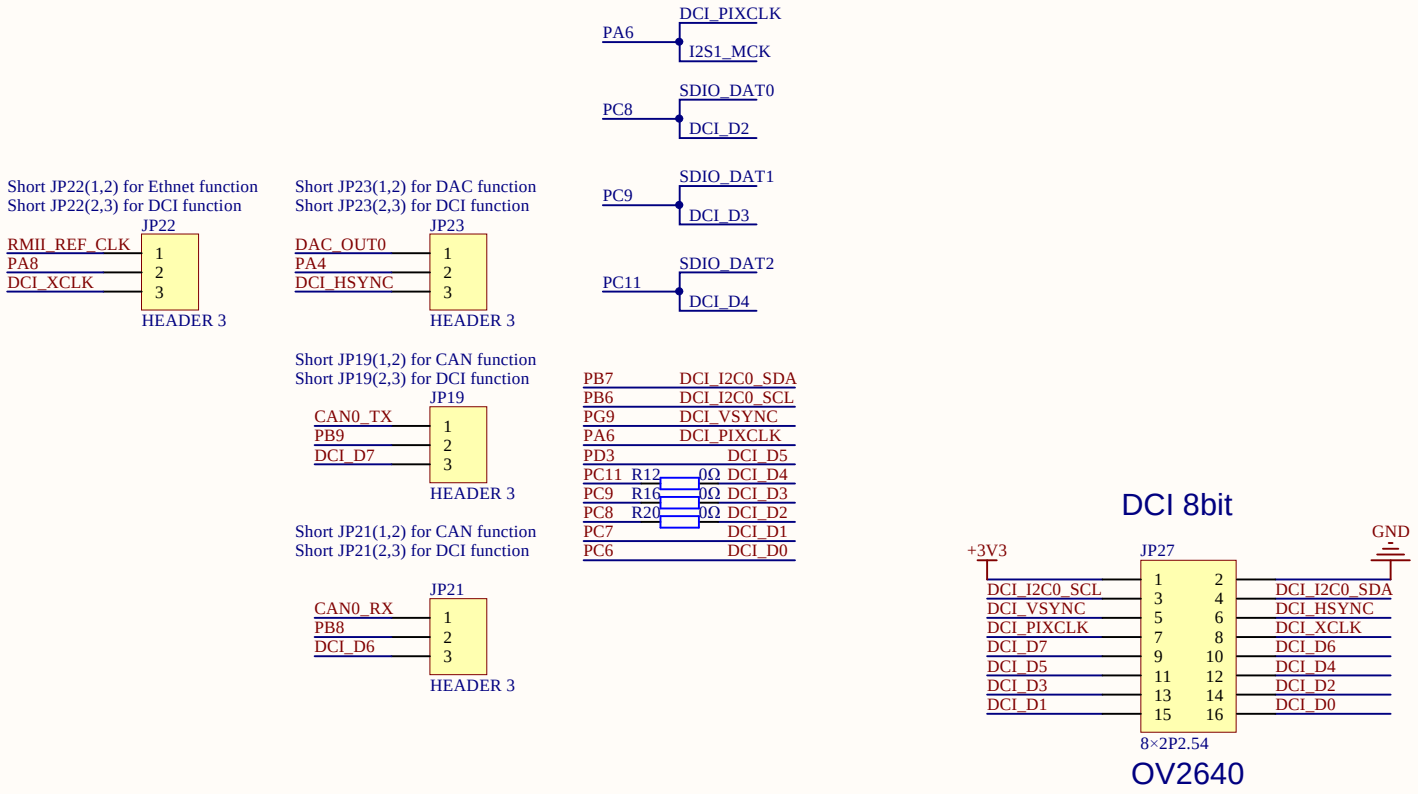
Company Name: GigaDevice		
File Name: AD_DA		
Revision: 1.0	Data: 2022-2	Author: wangzhan

CAN

PB8, PB9 are AFIOs, please refer to DCI schematic for right config



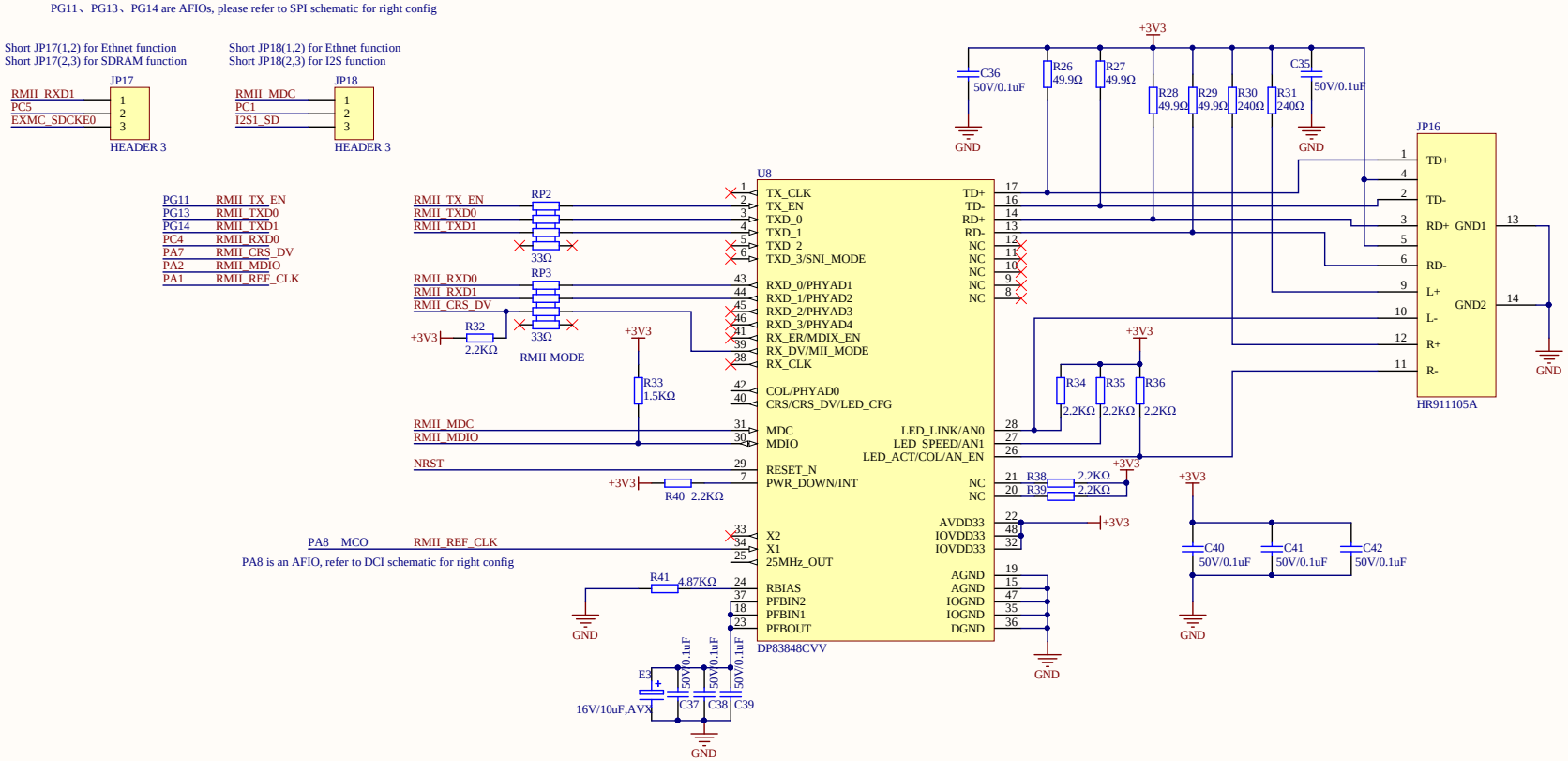
Company Name: GigaDevice		
File Name: CAN		
Revision: 1.0	Data: 2022-2	Author: wangzhan



DCI_8bit, TLI and SDRAM can be used at the same time

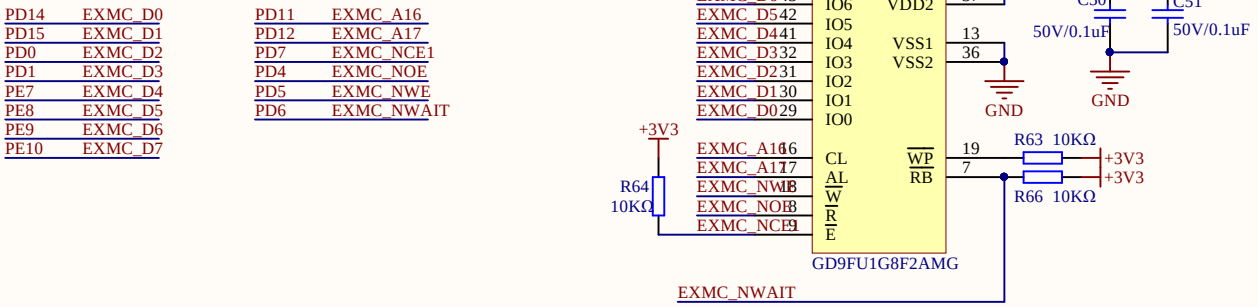
Company Name: GigaDevice		
File Name: DCI		
Revision: 1.0	Data: 2022-2	Author: wangzhan

Ethernet



Company Name: GigaDevice		
File Name: Ethernet		
Revision: 1.0	Data: 2022-2	Author: wangzhan

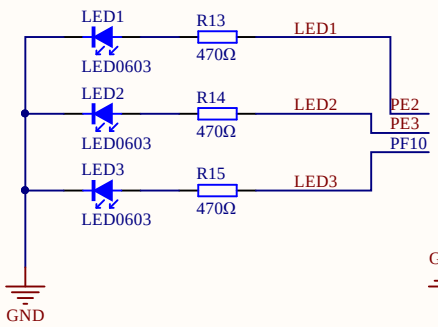
Nand Flash



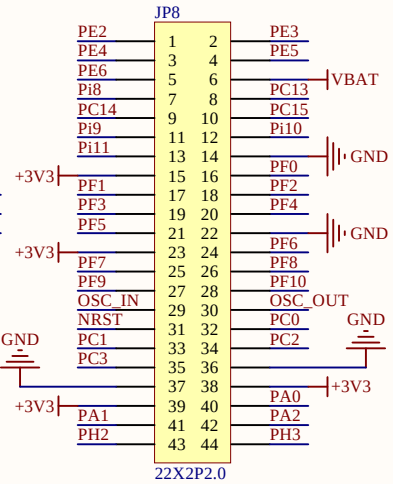
Company Name: GigaDevice	
File Name: EXMC	
Revision: 1.0	Data: 2022-2 / Author: wangzhan

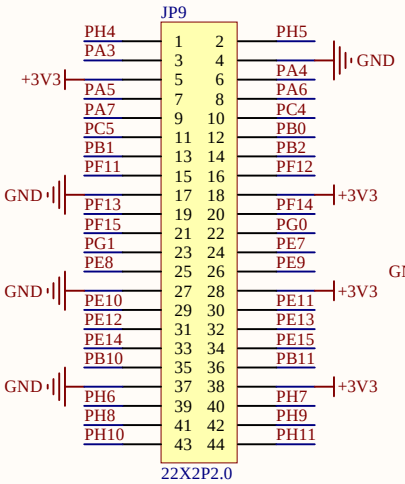
Extension Pin

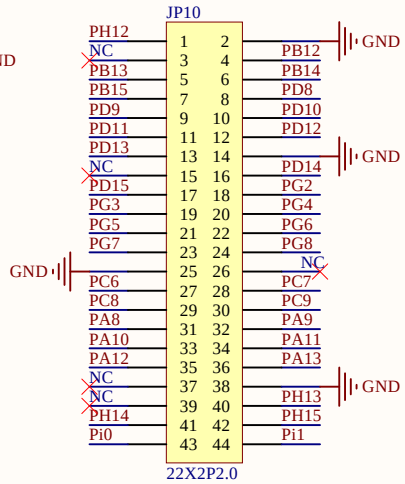
LED

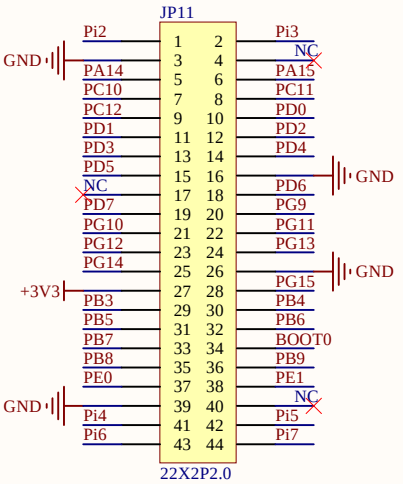


PF10 is an AFIO, refer to TLI schematic for right config



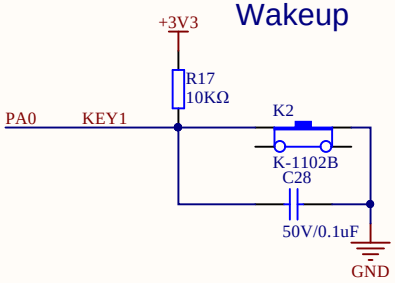




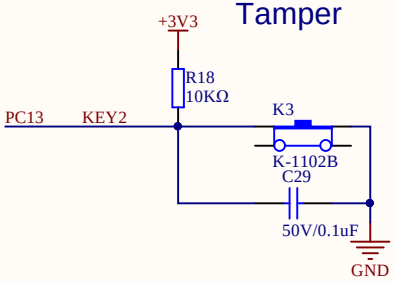


KEY

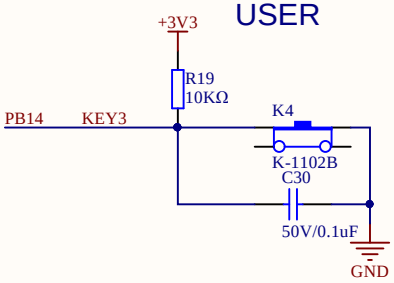
Wakeup



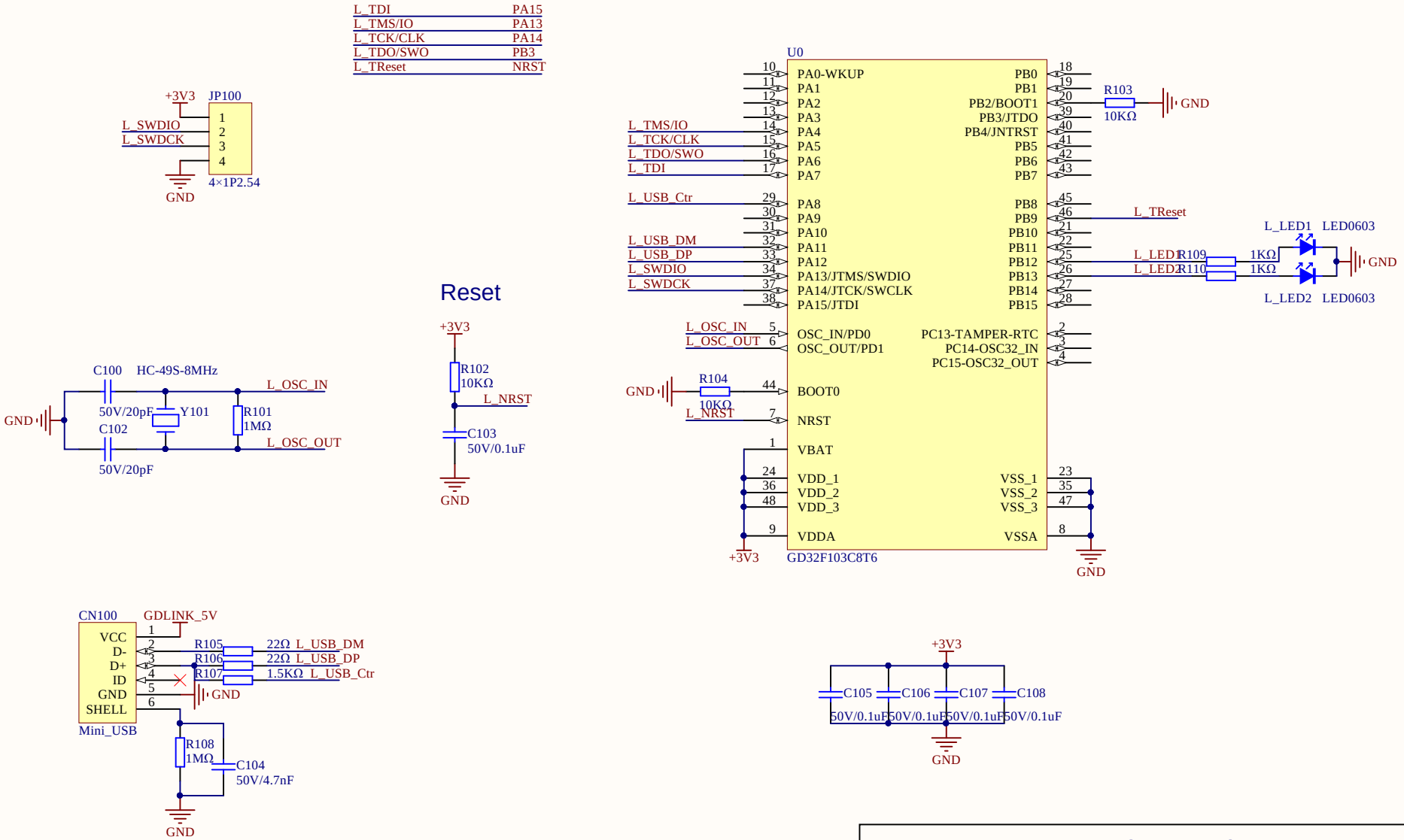
Tamper



USER

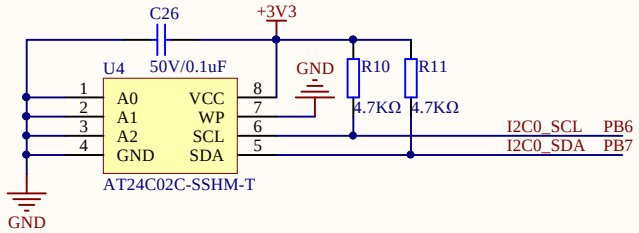


Company Name: GigaDevice		
File Name: Extension		
Revision: 1.0	Data: 2022-2	Author: wangzhan



Company Name: GigaDevice		
File Name: GDLink		
Revision: 1.0	Data: 2022-2	Author: XuFei

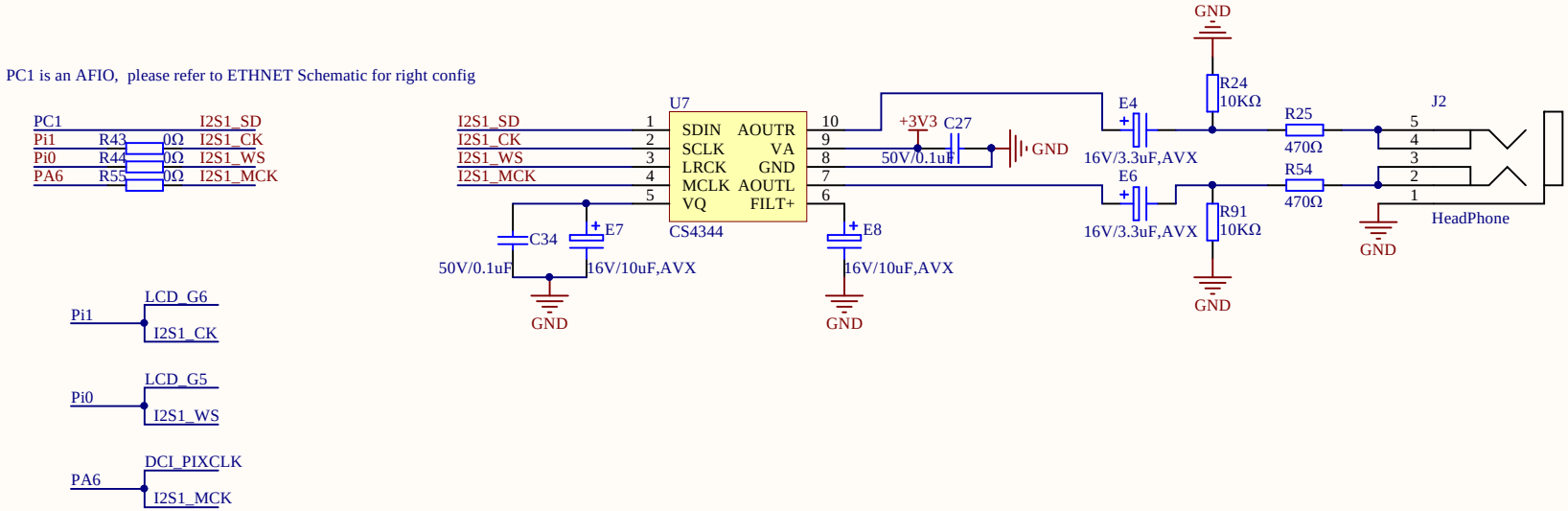
I2C



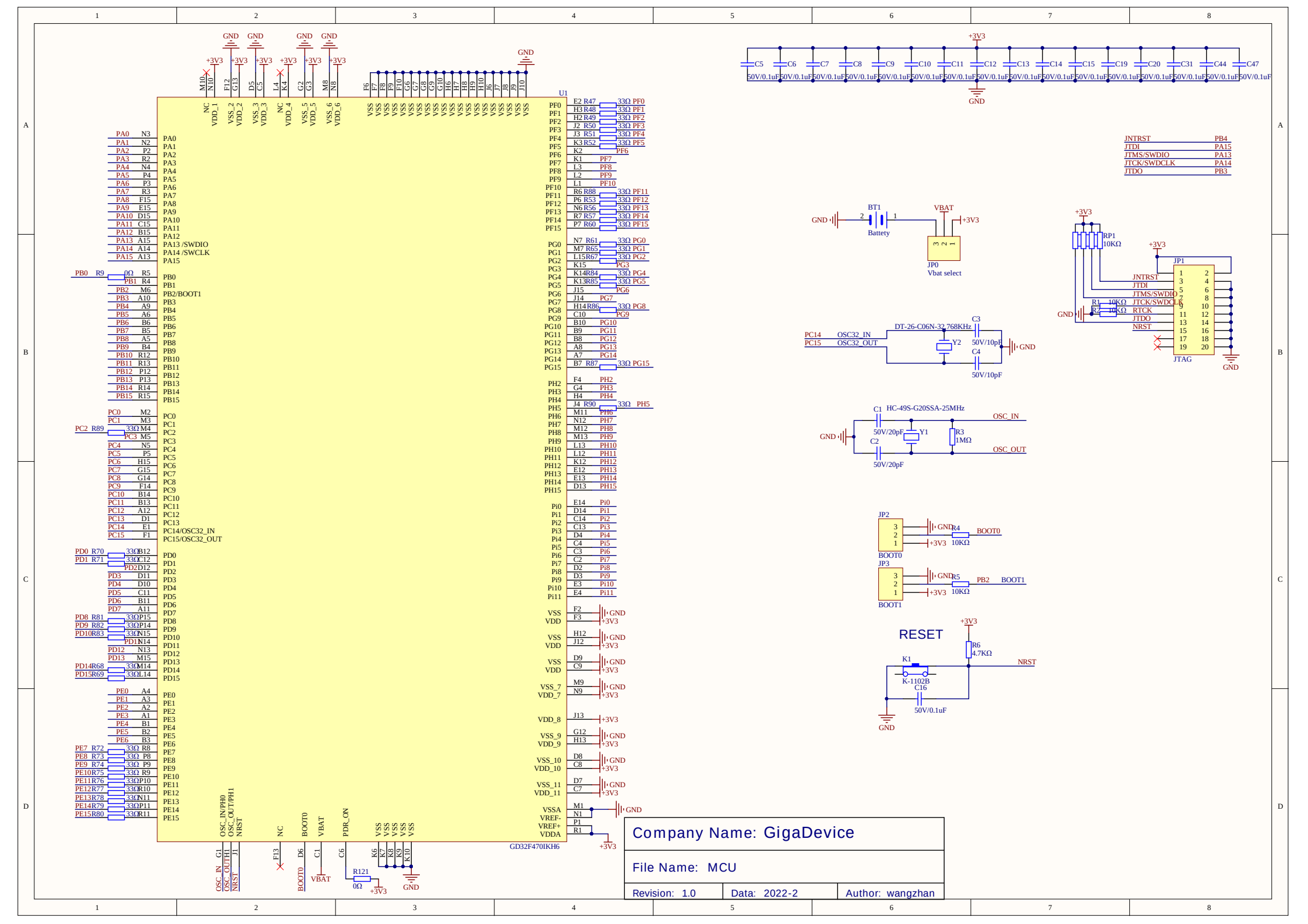
Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2022-2	Author: wangzhan

I2S

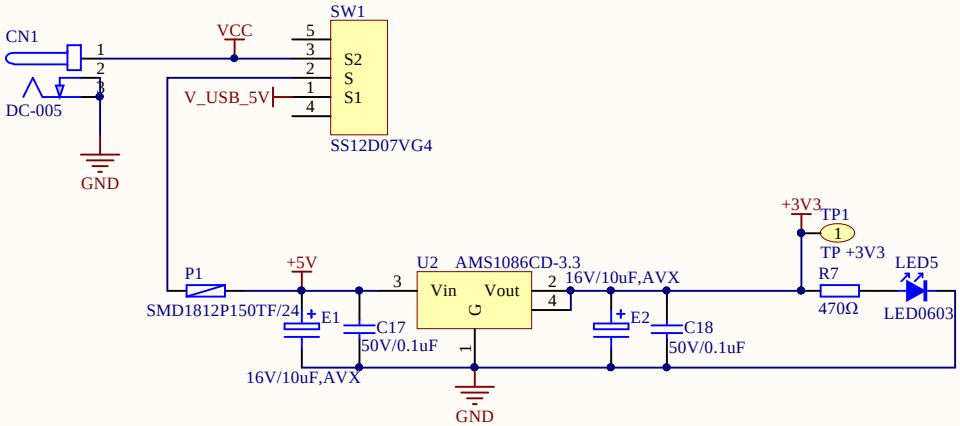
PC1 is an AFIO, please refer to ETHNET Schematic for right config

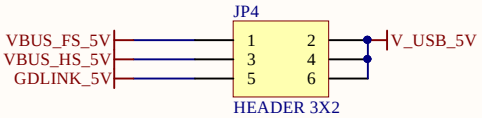


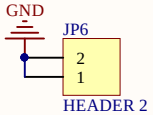
Company Name: GigaDevice		
File Name: I2S		
Revision: 1.0	Data: 2022-2	Author: wangzhan


U1
GD32F470IKH6
Company Name: GigaDevice
File Name: MCU
Revision: 1.0
Data: 2022-2
Author: wangzhan
Battery
Vbat select
DT-26-C06N-32.768KHz
HC-49S-G20SSA-25MHz
JTAG
RESET
K-1102B
BOOT0
BOOT1
JNTRST
JTDI
JTMS/SWDIO
JTCK/SWDCLK
JTDO
NRST
RTCK

POWER



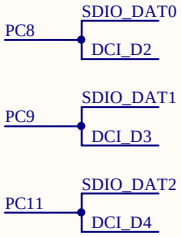




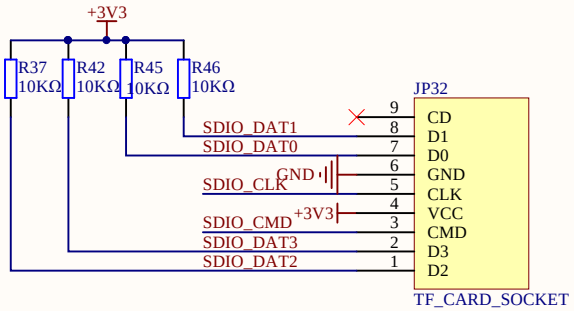
USB Power Supply selector

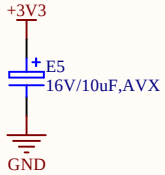
Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2022-2	Author: wangzhan

SDIO



PD2	SDIO_CMD
PC12	SDIO_CLK
PC8	SDIO_DAT0
PC9	SDIO_DAT1
PC10	SDIO_DAT2
PC11	SDIO_DAT3





Company Name: GigaDevice		
File Name: SDIO		
Revision: 1.0	Data: 2022-2	Author: wangzhan

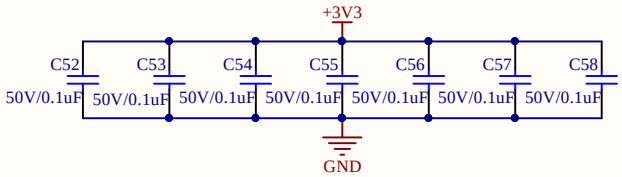
SDRAM

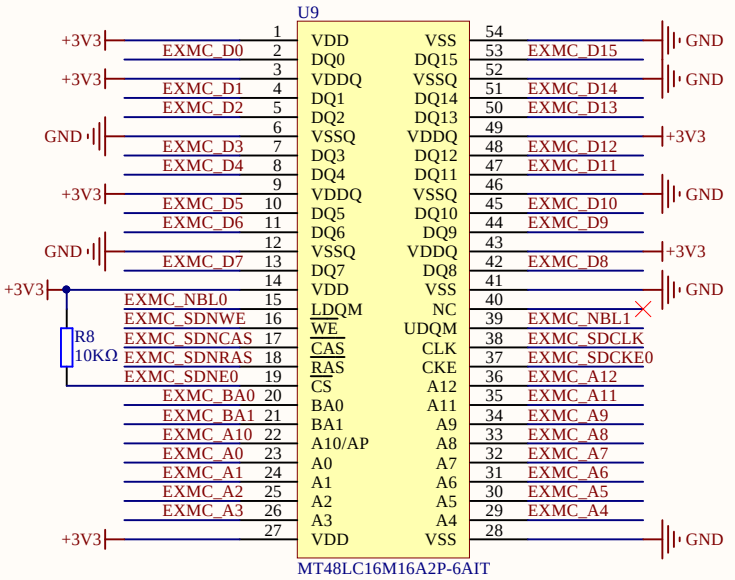
PF0	EXMC_A0
PF1	EXMC_A1
PF2	EXMC_A2
PF3	EXMC_A3
PF4	EXMC_A4
PF5	EXMC_A5
PF12	EXMC_A6
PF13	EXMC_A7
PF14	EXMC_A8
PF15	EXMC_A9
PG0	EXMC_A10
PG1	EXMC_A11
PG2	EXMC_A12

PD14	EXMC_D0
PD15	EXMC_D1
PD0	EXMC_D2
PD1	EXMC_D3
PE7	EXMC_D4
PE8	EXMC_D5
PE9	EXMC_D6
PE10	EXMC_D7
PE11	EXMC_D8
PE12	EXMC_D9
PE13	EXMC_D10
PE14	EXMC_D11
PE15	EXMC_D12
PD8	EXMC_D13
PD9	EXMC_D14
PD10	EXMC_D15

PC5 is AFIO, please refer to ETHNET schematic for right config

PE0	EXMC_NBL0
PE1	EXMC_NBL1
PC5	EXMC_SDCKE0
PG4	EXMC_BA0
PG5	EXMC_BA1
PG8	EXMC_SDCLK
PG15	EXMC_SDNCAS
PF11	EXMC_SDNRAS
PC2	EXMC_SDNE0
PH5	EXMC_SDNWE



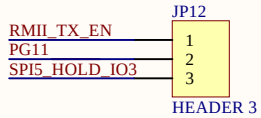


DCI_8bit, TLI and SDRAM can be used at the same time

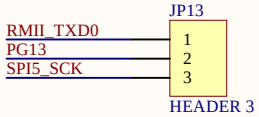
Company Name: GigaDevice		
File Name: SDRAM		
Revision: 1.0	Data: 2022-2	Author: wangzhan

Standard & Quad SPI Flash

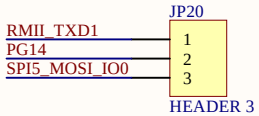
Short JP12(1,2) for Ethnet function
Short JP12(2,3) for SPI & TLI function

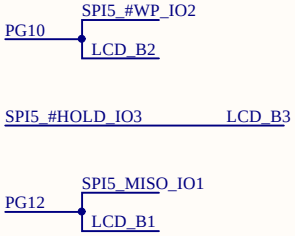


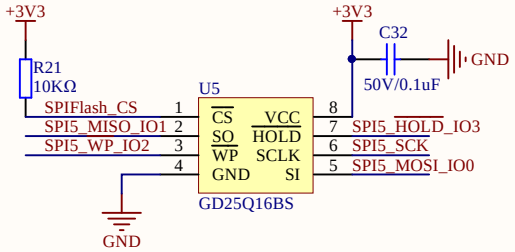
Short JP13(1,2) for Ethnet function
Short JP13(2,3) for SPI function



Short JP20(1,2) for Ethnet function
Short JP20(2,3) for SPI function







Company Name: GigaDevice		
File Name: SPI		
Revision: 1.0	Data: 2022-2	Author: wangzhan

PG10、PG11、PG12 are AFIO, please refer to SPI Schematic for right config

TLI

Pi3 LCD_Touch_PENIRQ
PF9 LCD_SPI4_MOSI
PF8 LCD_SPI4_MISO
PF7 LCD_SPI4_SCK
PF6 LCD_SPI4_NSS
PB15 LCD_PWM_BackLight
PH7 LCD_Touch_Busy

PH2 LCD_R0
PH3 LCD_R1
PH8 LCD_R2
PH9 LCD_R3
PH10 LCD_R4
PH11 LCD_R5
PH12 LCD_R6
PG6 LCD_R7

PE5 LCD_G0
PE6 LCD_G1
PH13 LCD_G2
PH14 LCD_G3
PH15 LCD_G4
Pi0 LCD_G5
Pi1 LCD_G6
Pi2 LCD_G7

Pi1 — I2S1_CK — LCD_G6
Pi0 — I2S1_WS — LCD_G5

PE4 LCD_B0
PG12 R117 0Ω LCD_B1
PG10 R118 0Ω LCD_B2
PG11 SPI5_HOLD_IO3 R119 0Ω LCD_B3
Pi4 LCD_B4
Pi5 LCD_B5
Pi6 LCD_B6
Pi7 LCD_B7

PG10 — LCD_B2 — SPI5_#WP_IO2
PG12 — LCD_B1 — SPI5_MISO_IO1

PG7 LCD_CLK
Pi10 LCD_HSYNC
Pi9 LCD_VSYNC

Short JP15(1,2) for TLI function
Short JP15(2,3) for LED function

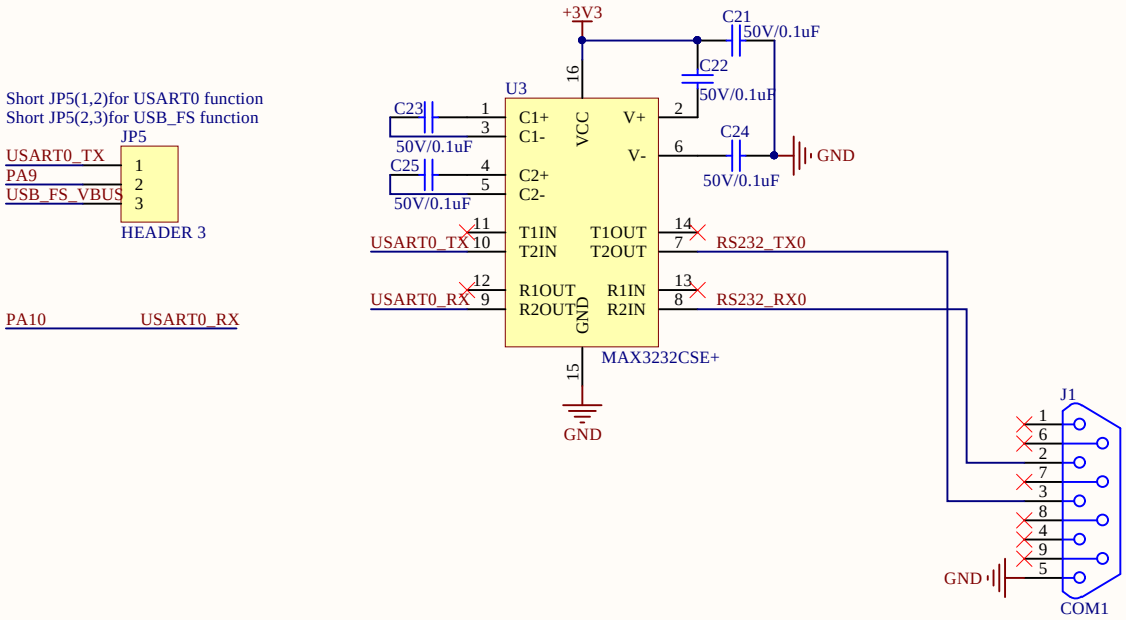
JP15
LCD_DE — 1
PF10 — 2
LED3 — 3
HEADER 3

JP30
+5V
LCD_Touch_PENIRQ — 1 — 2 — LCD_SPI4_MISO
LCD_SPI4_MOSI — 3 — 4 — LCD_SPI4_NSS
LCD_SPI4_SCK — 5 — 6
LCD_PWM_BackLight — 7 — 8 — GND
LCD_Touch_Busy — 9 — 10
LCD_R0 — 11 — 12 — LCD_R1
LCD_R2 — 13 — 14 — LCD_R3
LCD_R4 — 15 — 16 — LCD_R5
LCD_R6 — 17 — 18 — LCD_R7
LCD_G0 — 19 — 20 — LCD_G1
LCD_G2 — 21 — 22 — LCD_G3
LCD_G4 — 23 — 24 — LCD_G5
LCD_G6 — 25 — 26 — LCD_G7
LCD_B0 — 27 — 28 — LCD_B1
LCD_B2 — 29 — 30 — LCD_B3
LCD_B4 — 31 — 32 — LCD_B5
LCD_B6 — 33 — 34 — LCD_B7
LCD_CLK — 35 — 36
LCD_HSYNC — 37 — 38 — LCD_VSYNC
LCD_DE — 39 — 40
20X2P2.54
GND

DCI_8bit, TLI and SDRAM can be used at the same time

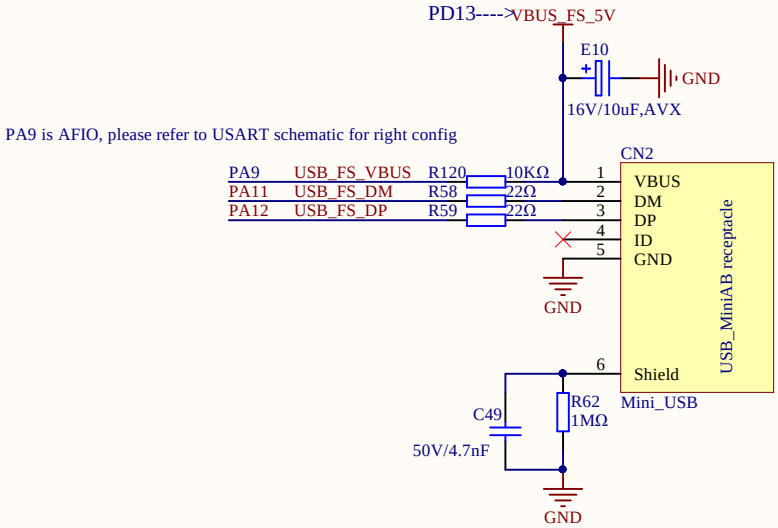
Company Name: GigaDevice		
File Name: TLI		
Revision: 1.1	Data: 2022-2	Author: wangzhan

USART0

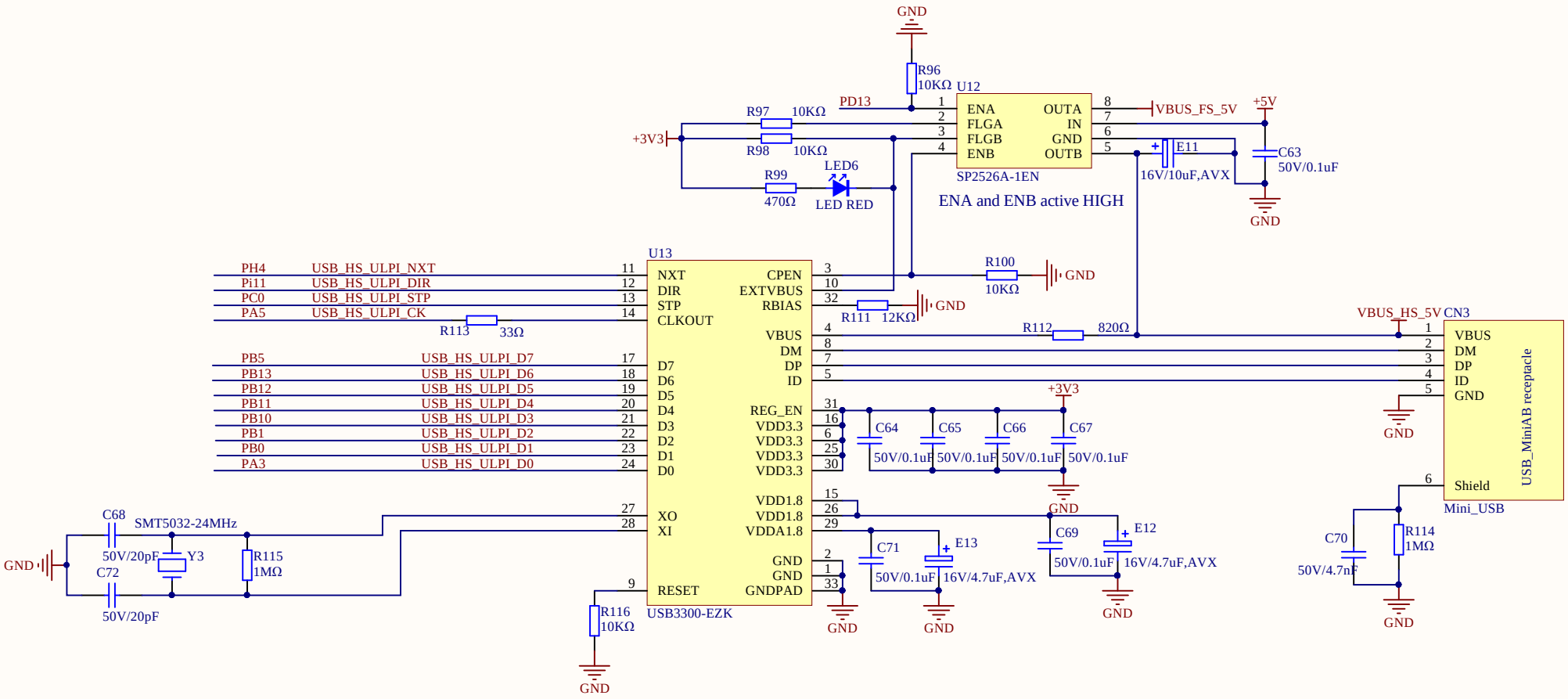


Company Name: GigaDevice		
File Name: USART		
Revision: 1.0	Data: 2022-2	Author: wangzhan

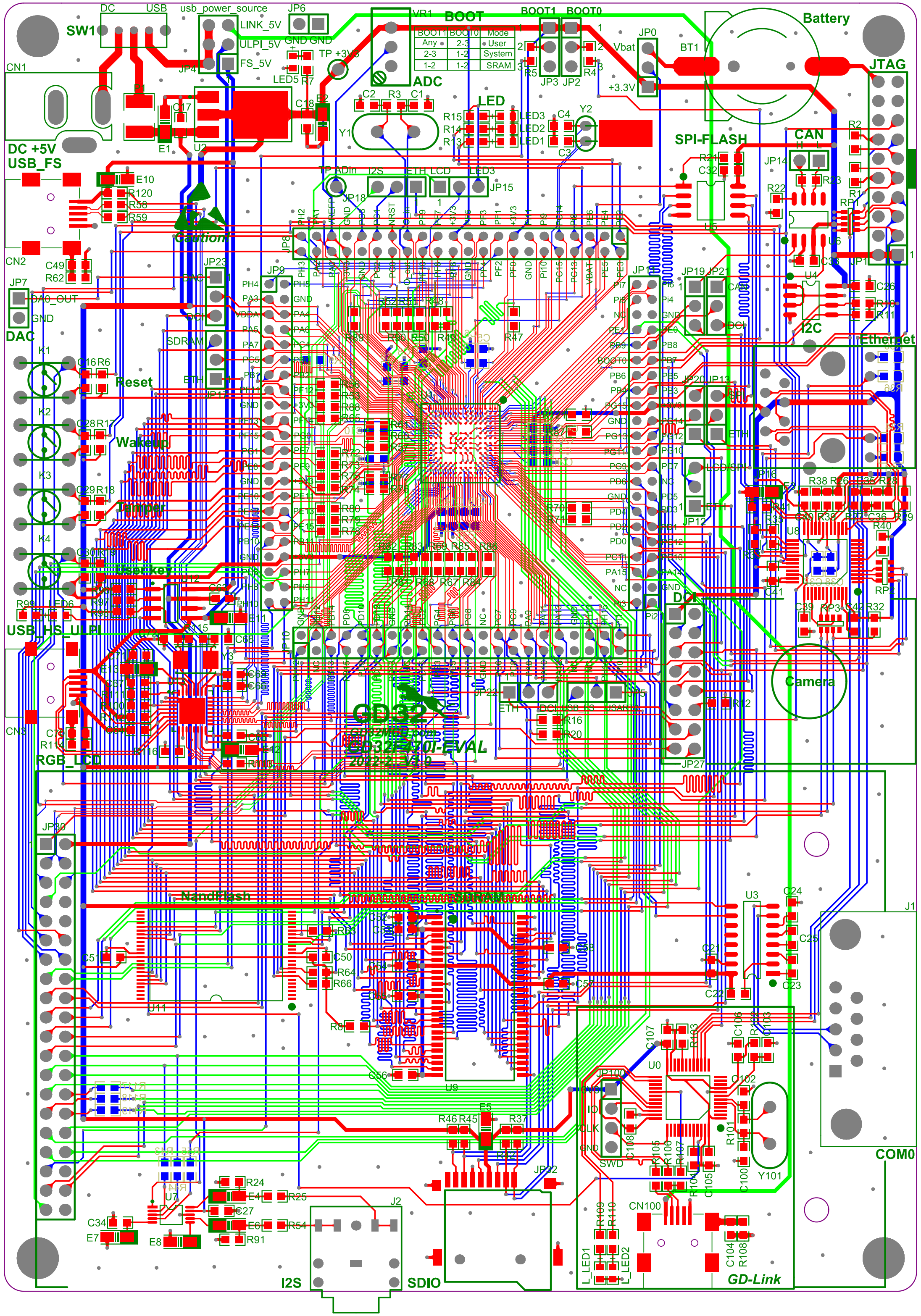
"VBUS_FS_5V control（active HIGH）" see USB_HS schematic



Company Name: GigaDevice		
File Name: USB_FS		
Revision: 1.1	Data: 2022-2	Author: wangzhan



Company Name: GigaDevice		
File Name: USB_HS		
Revision: 2.0	Data: 2022-2	Author: wangzhan



SW1
DC
USB
usb_power_source
JP6
LINK_5V
ULPI_5V
FS_5V
BOOT
BOOT1
BOOT0
Battery
JTAG
ADC
LED
SPI-FLASH
CAN
I2C
Ethernet
DC +5V
USB_FS
DAC
Reset
Wakeup
Tamper
User key
USB_HS_ULPI
RGB_LCD
Camera
GD32
NandFlash
SDRAM
COM0
I2S
SDIO
GD-Link