



AD-UMD9N Digital Transistor (Built-In Resistors)

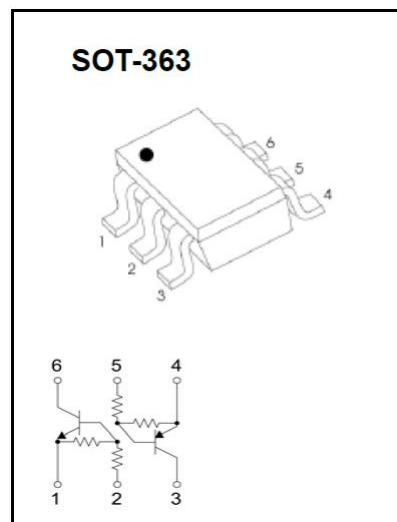
AD-UMD9N Dual digital transistor (NPN+PNP)

FEATURES

- AD-DTA114Y* and AD-DTC114Y* series chips in a package
- AEC-Q101 qualified

MARKING

$\overline{D}9$



MAXIMUM RATINGS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
NPN collector-emitter voltage	V_{CEO}	50	V
NPN collector-base voltage	V_{CBO}	50	
NPN input voltage	V_{IN}	-6 ~ 40	
PNP collector-emitter voltage	V_{CEO}	-50	V
PNP collector-base voltage	V_{CBO}	-50	
PNP input voltage	V_{IN}	-40 ~ 6	
Each output current	I_{O}	70	mA
Each peak collector current	I_{CM}	100	mA
Maximum power dissipation	$P_{\text{D}}^{1) 2)}$	150	mW
Junction to ambient thermal resistance	$R_{\Theta\text{JA}}^{2)}$	833	$^\circ\text{C/W}$
Junction to lead thermal resistance	$R_{\Theta\text{JL}}^{2)}$	314	$^\circ\text{C/W}$
Maximum power dissipation	$P_{\text{D}}^{1) 3)}$	185	mW
Junction to ambient thermal resistance	$R_{\Theta\text{JA}}^{3)}$	675	$^\circ\text{C/W}$
Junction to lead thermal resistance	$R_{\Theta\text{JL}}^{3)}$	237	$^\circ\text{C/W}$
Operating junction and storage temperature range	T_j, T_{stg}	-55 ~ 150	$^\circ\text{C}$

1) $T_a = 25^\circ\text{C}$

2) One junction heated, minimum FR4 pad

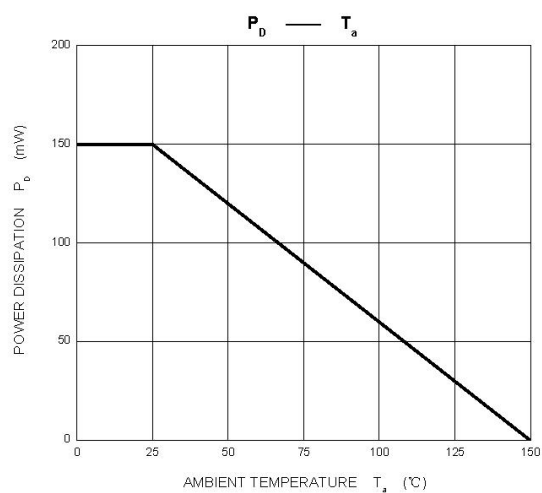
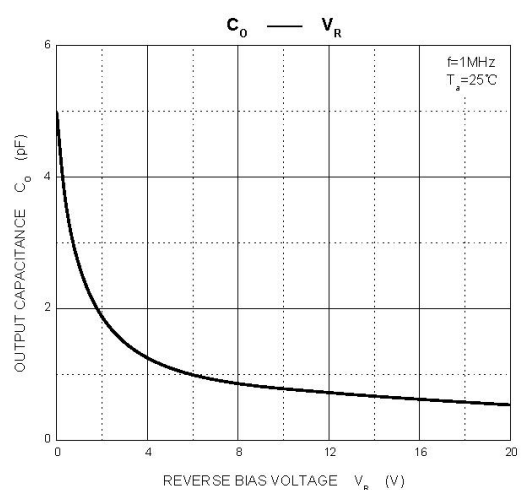
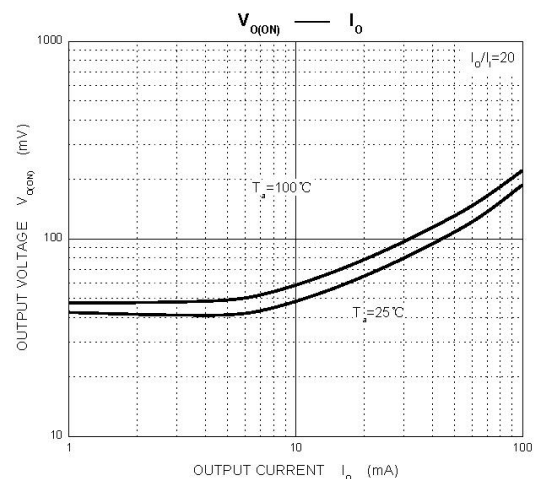
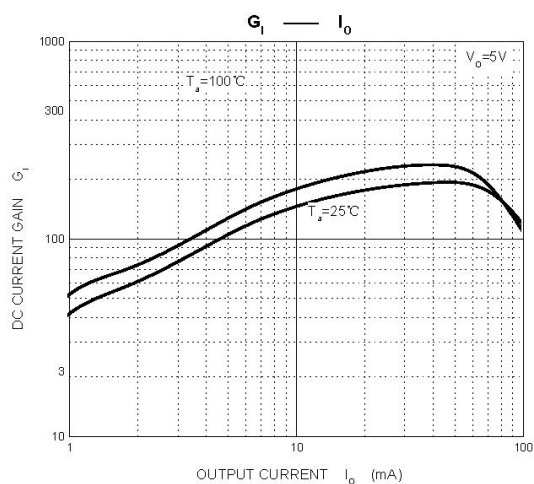
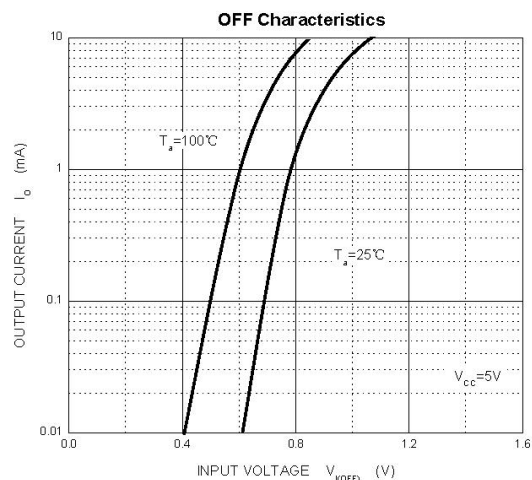
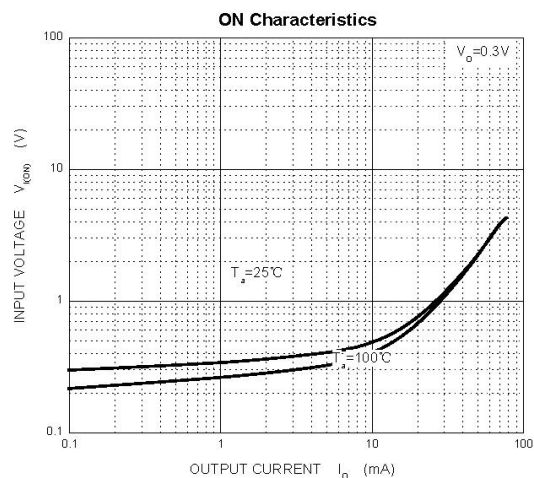
3) Both junctions heated, minimum FR4 pad

ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

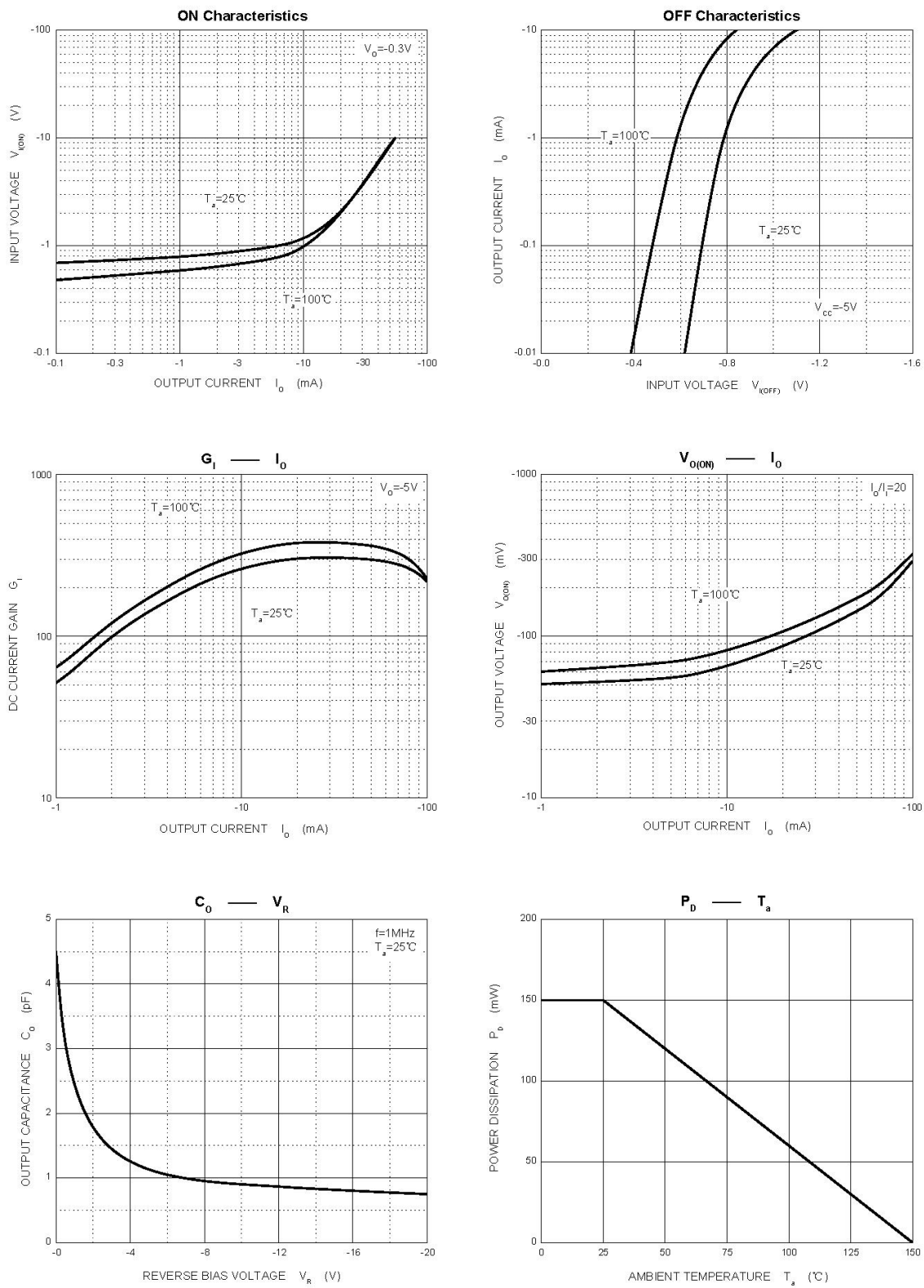
Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Input voltage	$V_{\text{I(off)}}$	$V_{\text{CC}} = 5\text{V}, I_{\text{O}} = 100\mu\text{A}$	0.3	-	-	V
	$V_{\text{I(on)}}$	$V_{\text{O}} = 0.3\text{V}, I_{\text{O}} = 1\text{mA}$	-	-	1.4	
Output voltage	$V_{\text{O(on)}}$	$I_{\text{O}}/I_{\text{I}} = 5\text{mA}/0.25\text{mA}$	-	-	0.3	V
Input current	I_{I}	$V_{\text{I}} = 5\text{V}$	-	-	0.88	mA
Output current	$I_{\text{O(off)}}$	$V_{\text{CC}} = 50\text{V}, V_{\text{I}} = 0\text{V}$	-	-	0.5	μA
DC current gain	G_{I}	$V_{\text{O}} = 5\text{V}, I_{\text{O}} = 5\text{mA}$	68	-	-	-
Input resistance	R_{I}	-	7	10	13	$\text{k}\Omega$
Resistance ratio	$R_{\text{I}}/R_{\text{I}}$	-	3.7	4.7	5.7	-
Transition frequency	f_{T}	$V_{\text{O}} = 10\text{V}, I_{\text{O}} = 5\text{mA}, f = 100\text{MHz}$	-	250	-	MHz

TYPICAL CHARACTERISTICS

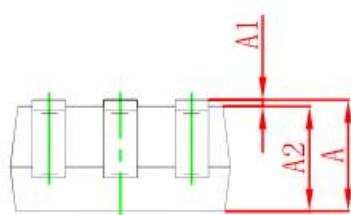
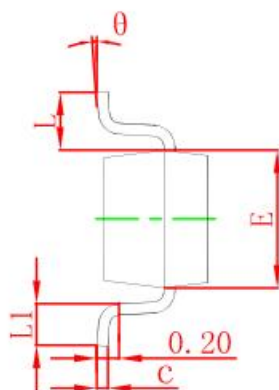
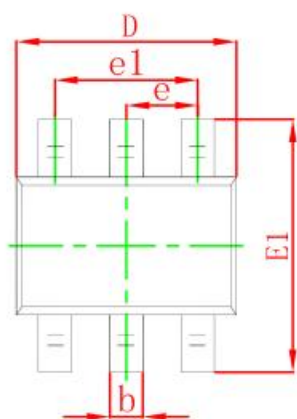
AD-DTC114Y



AD-DTA114Y

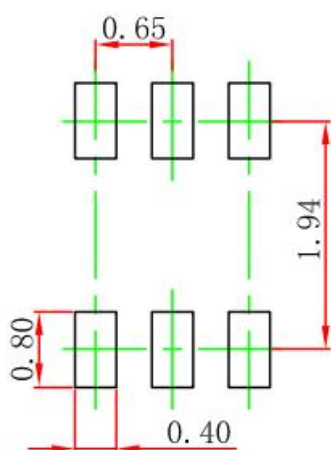


SOT-363 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

SOT-363 SUGGESTED PAD LAYOUT

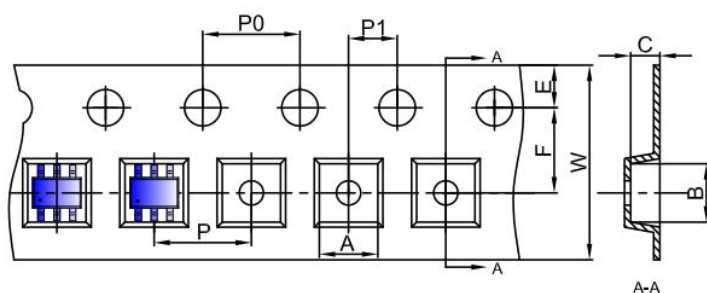


Note:

1. Controlling dimension in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purpose only.

SOT-363 TAPE AND REEL

SOT-363 Embossed Carrier Tape

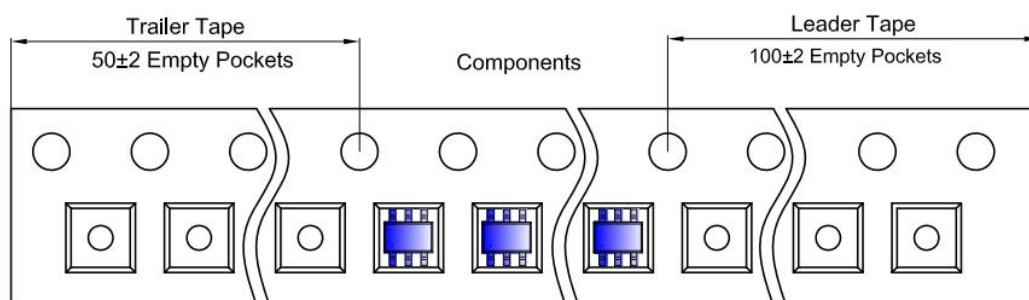


Packaging Description:

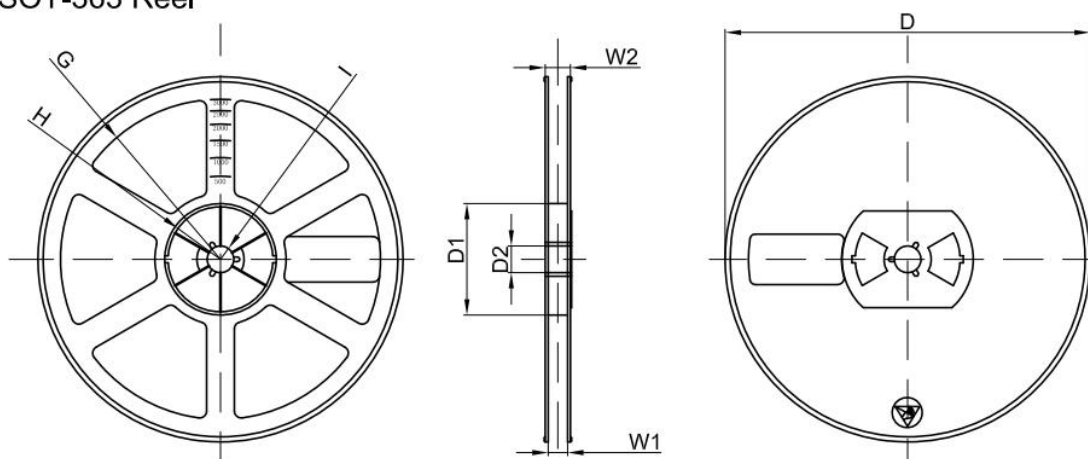
SOT-363 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 7" or 17.8cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
SOT-363	2.25	2.55	1.20	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

SOT-363 Tape Leader and Trailer



SOT-363 Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
7" Dia	Ø178.00	54.40	13.00	R78.00	R25.60	R6.50	9.50	12.30

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
3000 pcs	7 inch	45,000 pcs	203×203×195	180,000 pcs	438×438×220	

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