

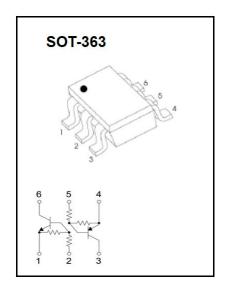
# JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD.

# **AD-UMD15N Digital Transistor (Built-In Resistors)**

# **AD-UMD15N** Dual digital transistor (NPN+PNP)

#### **FEATURES**

- AD-DTC143E and AD-DTA143E chips in a package
- Mounting possible with SOT-363 automatic mounting machines
- Transistor elements are independent, eliminating interference
- Mounting cost and area be cut in half
- AEC-Q101 qualified



#### **MARKING**

D15

**MAXIMUM RATINGS NPN TRANSISTOR (T<sub>i</sub> = 25°C unless otherwise specified)** 

·	. ,	<u> </u>		
Parameter	Symbol	Value	Unit	
Supply voltage	Vcc	50	٧	
Input voltage	V <sub>IN</sub>	-10 ~ 30	V	
Output current	lo	100	mA	
Peak collector current	I <sub>C(MAX)</sub>	100	mA	
Maximum power dissipation	P <sub>D</sub>	150	mW	
Operating junction and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ 150	°C	

# **ELECTRICAL CHARACTERISTICS NPN TRANSISTOR (Tj = 25°C unless otherwise specified)**

Parameter	Parameter Symbol Test condition		Min	Тур	Max	Unit
Input voltage	V <sub>I(off)</sub>	$V_{CC} = 5V, I_{O} = 100 \mu A$		-	-	V
	V <sub>I(on)</sub>	V <sub>O</sub> = 0.3V, I <sub>O</sub> = 20mA	-	-	3	V
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> = 10mA/0.5mA	-	-	0.3	V
Input current	I <sub>I</sub>	V <sub>I</sub> = 5V	-	-	1.8	mA
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V	-	-	0.5	μΑ
DC current gain	Gı	V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA	20	-	-	-
Input resistance	R <sub>1</sub>	-	3.29	-	6.11	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	-	1.2	-
Transition frequency	f⊤	V <sub>O</sub> = 10V, I <sub>O</sub> = 5mA, f = 100MHz	-	250	-	MHz

# MAXIMUM RATINGS PNP TRANSISTOR (T<sub>j</sub> = 25°C unless otherwise specified)

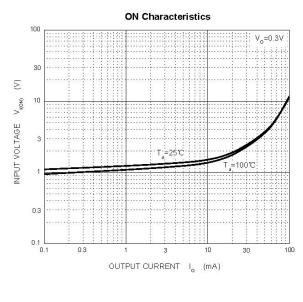
Parameter	Symbol	Value	Unit
Supply voltage	Vcc	-50	٧
Input voltage	V <sub>IN</sub>	-30 ~ 10	٧
Output current	I <sub>O</sub>	-100	mA
Peak collector current	I <sub>C(MAX)</sub>	-100	mA
Maximum power dissipation	PD	150	mW
Operating junction and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ 150	Ĉ

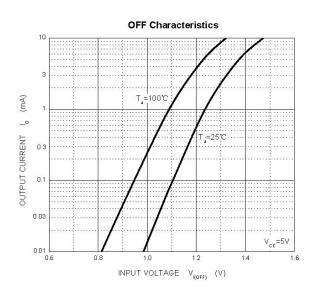
## ELECTRICAL CHARACTERISTICS PNP TRANSISTOR (T<sub>i</sub> = 25°C unless otherwise specified)

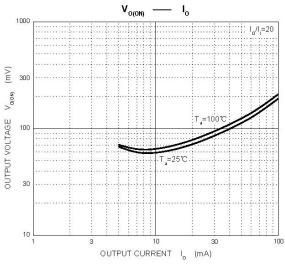
ELECTRICAL CHARACTERIO TICOTTAL TRANSPORTER (1) 20 C unicos cinervise specifica)										
Parameter	Symbol	Test condition	Min	Тур	Max	Unit				
Input voltage	V <sub>I(off)</sub>	$V_{CC} = -5V$ , $I_{O} = -100\mu A$		-	-	V				
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA	-	-	-3	] <b>v</b>				
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA	-	-	-0.3	V				
Input current	l <sub>l</sub>	V <sub>I</sub> = -5V	-	-	-1.8	mA				
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = -50V, V <sub>I</sub> = 0V	-	-	-0.5	μA				
DC current gain	Gı	V <sub>O</sub> = -5V, I <sub>O</sub> = -10mA	30	-	-	-				
Input resistance	R <sub>1</sub>	-	3.29	-	6.11	kΩ				
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	-	1.2					
Transition frequency	f⊤	$V_0 = -10V$ , $I_0 = -5mA$ , $f = 100MHz$	-	250	-	MHz				

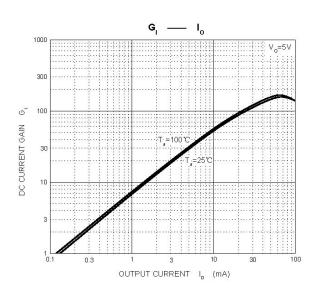
## TYPICAL CHARACTERISTICS

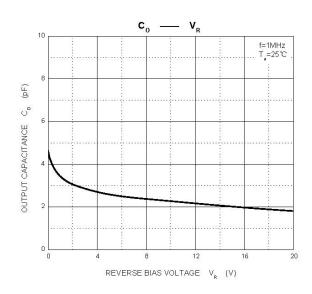
## **NPN Transistor**

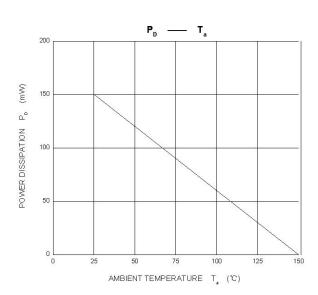






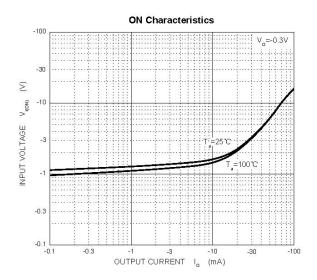


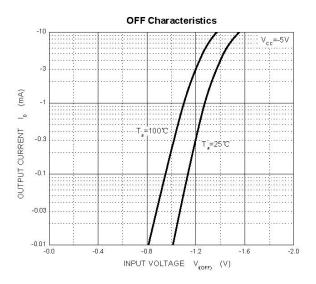


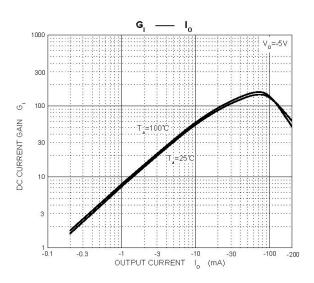


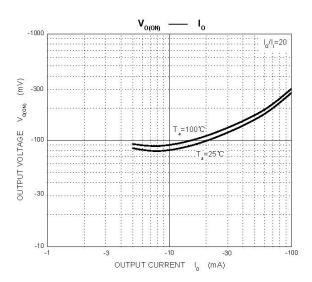
## TYPICAL CHARACTERISTICS

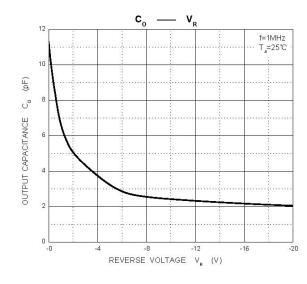
## **PNP Transistor**

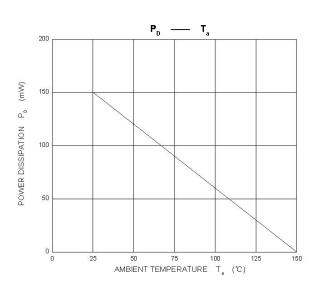




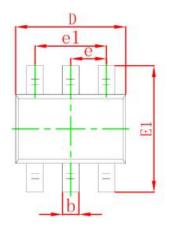


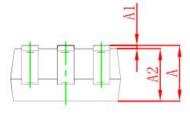


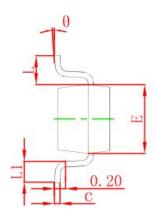




## **SOT-363 PACKAGE OUTLINE DIMENSIONS**

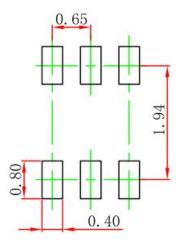






Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
С	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
е	0.650	) TYP	0.026	TYP
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021	IREF
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

## **SOT-363 SUGGESTED PAD LAYOUT**

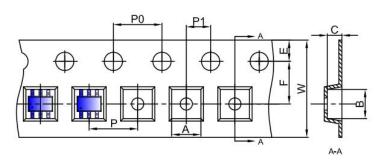


#### Note:

- 1. Controlling dimension in millimeters.
- 2. General tolerance: ±0.05mm.
- 3. The pad layout is for reference purpose only.

## **SOT-363 TAPE AND REEL**

## SOT-363 Embossed Carrier Tape

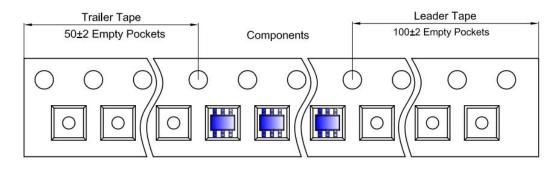


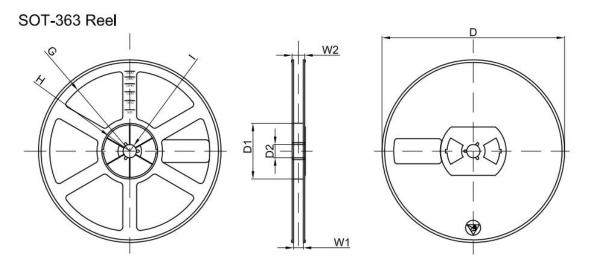
#### Packaging Description:

SOT-363 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 7" or 17.8cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

				Dimensions a	are in millime	ter				
Pkg type	Α	В	С	d	E	F	P0	Р	P1	W
SOT-363	2.25	2.55	1.20	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

## SOT-363 Tape Leader and Trailer





Dimensions are in millimeter									
Reel Option	D	D1	D2	G	Н	1	W1	W2	
7"D <b>i</b> a	Ø178.00	54.40	13.00	R78.00	R25.60	R6.50	9.50	12.30	

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
3000 pcs	7 inch	45,000 pcs	203×203×195	180,000 pcs	438×438×220	

#### **PUBLISHED BY**

JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD.

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